

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Douglass
Assignee: Xilinx, Inc.
Title: "Method of Designing Integrated Circuit Having Both Configurable and Fixed Logic Circuitry"

Serial No.: 10/092,051 File Date: 03-04-02
Examiner: Unknown Art Unit: 2811
Docket No.: X-923 US

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

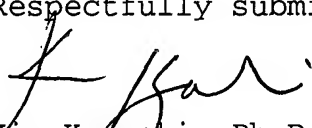
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the one (1) reference listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,


Kim Kanzaki, Ph.D.
Attorney for Applicant
Reg. No. 37,652

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D. C. 20231, on December 10, 2002

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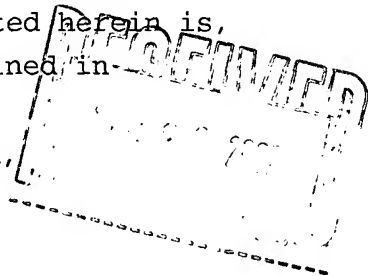

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Complete if Known

Application / Conf. N .	10/092,051 / 7802
Filing Date	March 04, 2002
First Named Invent r	Stephen M. Douglass
Art Unit	2811
Examiner Name	Unknown
Attorney Docket Number	X-923 US

(use as many sheets as necessary)

Sheet	1	of	1
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[illegible][illegible]

**Examiner
Signature**

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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